## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Appln. No.	:									
Filed	:	03/17/2004				Group A	rt Unit:			
Title	:	SELF-ALIGNED SI MEMORY AND FA				Examine	r:			
		DECLARATION	FOR UTILIT (37 C.F.R		NT API	PLICATI	ON			
As a be	low named	inventor, I hereby de	clare that my i	mailing ad	ldress an	d citizens	hip are as	stated be	low.	
	ral names ar	original, first and sole e listed below) of the								
SELF-ALIGNE	ED SPLIT-C	GATE NAND FLASH	I MEMORY A	AND FAB	RICAT	ON PRO	CESS			
the spec	cification of	which:								
⊠ is a	ttached here	eto OR								
was filed on as United States Application Number or PCT International Application Number and amended on (if applicable).								ber		
		have reviewed and u amendment specifications			of the ab	ove-ident	ified spec	ification,	includir	ng the
37 C.F.R. 1.56,	including for	duty to disclose infor or continuation-in-paication and the nation	rt applications	, material	informa	tion whic	h became	available	betwee	
patent or inven other than the U	tor's certific United State patent or in	eign priority benefits cate, or 365(a) of any s of America, listed by ventor's certificate, of ity is claimed.	PCT international pelow and have	onal appli e also ider	cation watified be	hich desi	gnated at lacking t	least one he box, a	country ny foreig	gn
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I hereby claim	the benefit i	under 35 U.S.C. 119(	e) of any Unit	ed States p	provisio	nal applic	ation(s) lis	sted belov	w.	
	Applica	tion Number(s)			Fil	ing Date	(MM/DD/	YYYY)		

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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